



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....10/635,715  
Confirmation No. ....2007  
Filing Date .....August 5, 2003  
Inventor..... W.G. Scott  
Assignee .....Micron Technology, Inc.  
Group Art Unit.....2812  
Examiner .....Richard A. Booth  
Attorney's Docket No. ....MI22-2375  
Customer No. ....021567  
Title: Methods of Forming Transistor Gates; and Methods of Forming Programmable  
Read-Only Memory Constructions

**RESPONSE TO MARCH 22, 2005 OFFICE ACTION**

To: Commissioner for Patents  
P.O. Box 1450  
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**AMENDMENTS**